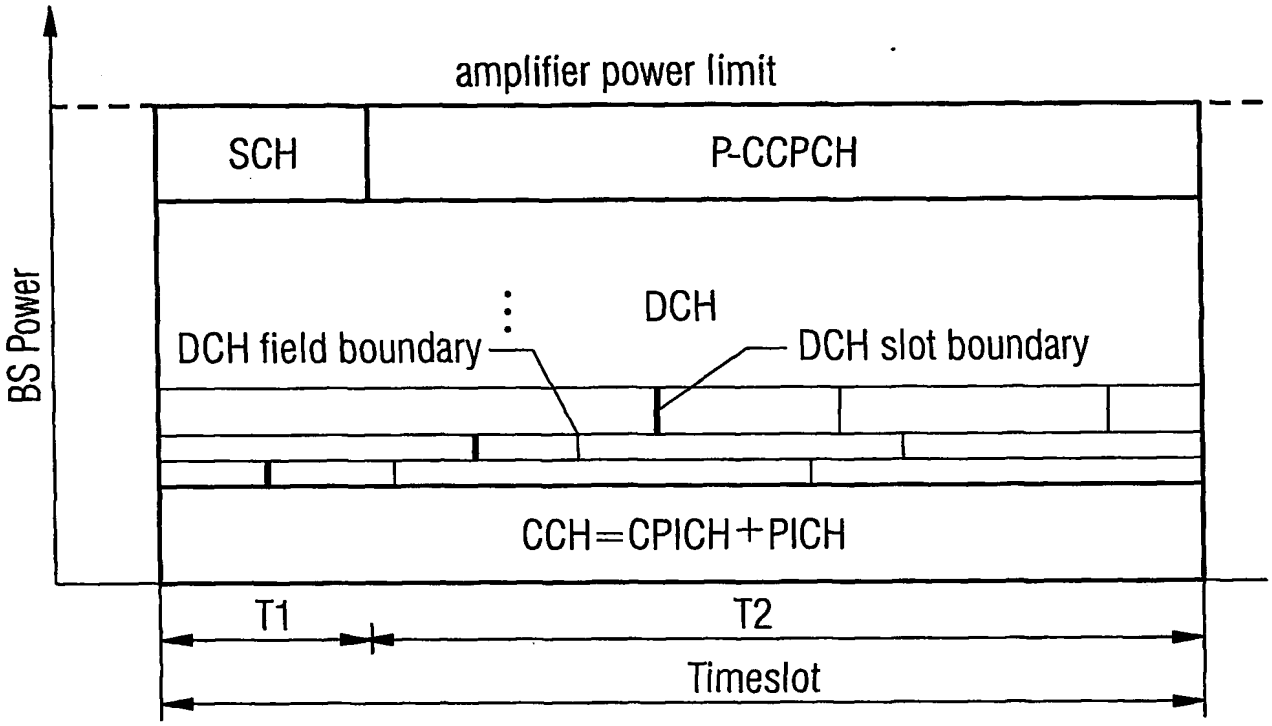
FIG 1


amplifier power limit
SCH
P-CCPCH
BS Power
DCH
DCH field boundary
DCH slot boundary
CCH=CPICH+PICH
T1
T2
Timeslot

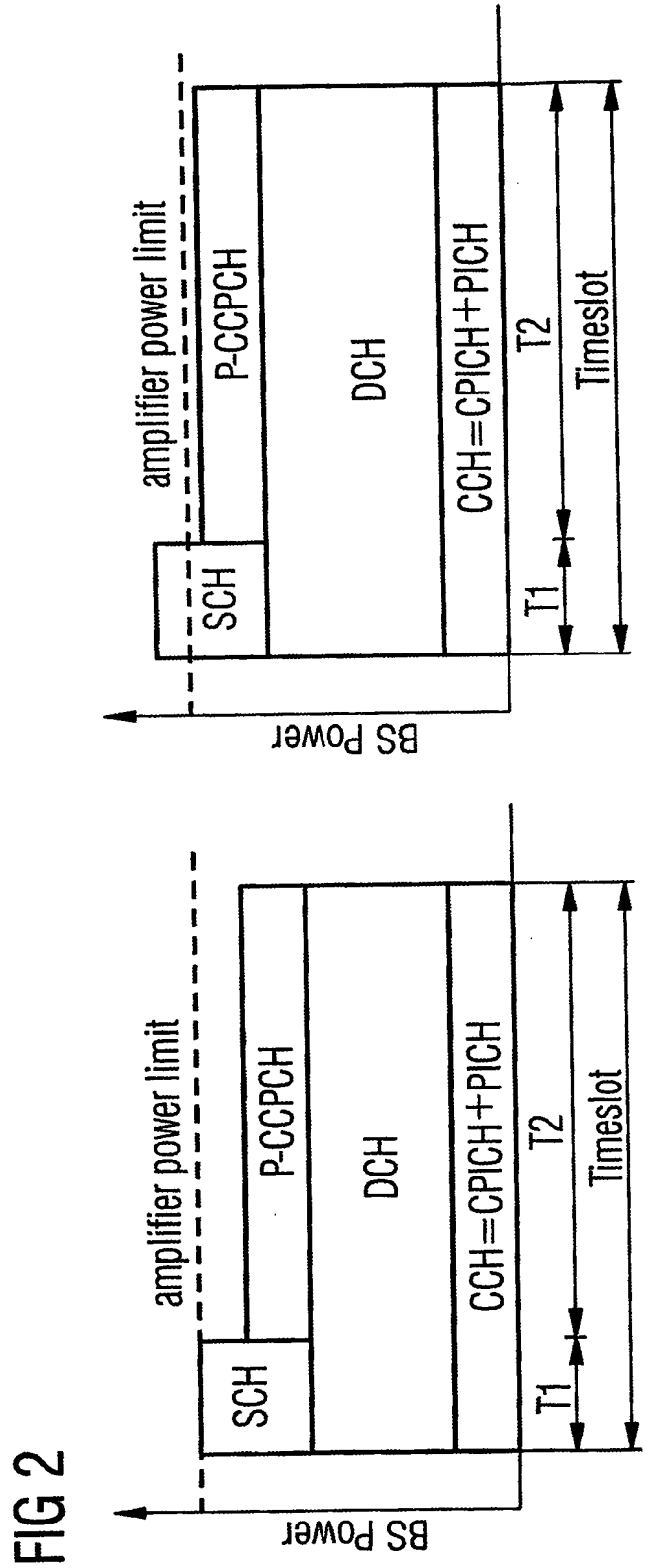
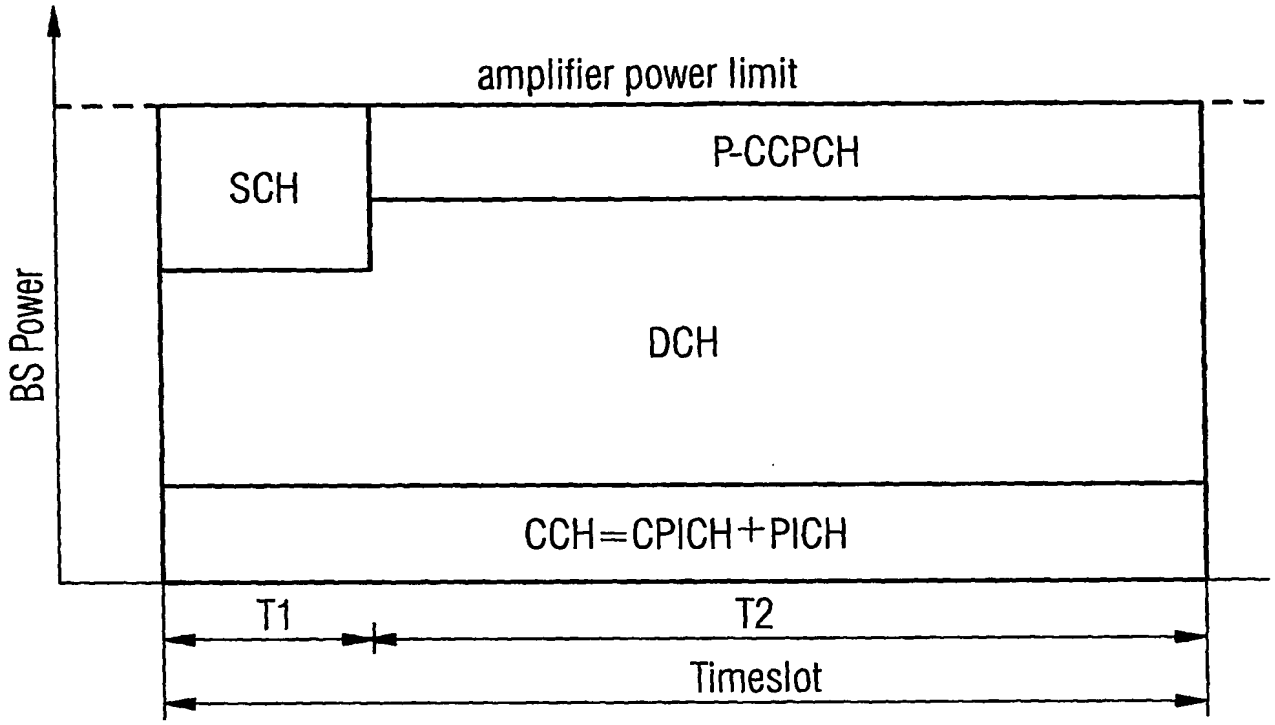

amplifier power limit
P-CCPCH
DCH
CCH=CPICH+PICH
T2
Timeslot
SCH
T1
BS Power
amplifier power limit
P-CCPCH
DCH
CCH=CPICH+PICH
T2
Timeslot
SCH
T1
BS Power

FIG 2

FIG 3


amplifier power limit
SCH
P-CCPCH
DCH
CCH=CPICH+PICH
BS Power
T1
T2
Timeslot